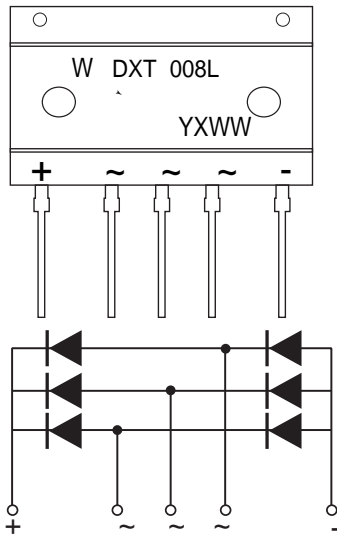


## Low VF Three-phase Bridge Rectifiers



) HDWXUHV

‡\*ODVV 3DVVLYDWHG &KLS -XQFW

‡/RZ ,550

‡/RZ 9)

‡+LJK 9550

‡6SHFLDO IUDPH GHVLJQ IRU KHD

%HQHILWV

- Case: DXT
- Terminals: Solderable Per MIL-STD-750
- Reduced power loss and switching transistor

3 D U D P H W H U	Symbols	' ; T60 /	8 Q L W V
Maximum Repetitive Peak Reverse Voltage	VRRM	800	V
Maximum RMS voltage	VRMS	560	V
Maximum DC Blocking Voltage	VDC	800	V
Average Rectified Output Current	Io	60	A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	IFSM	450	A
I <sup>2</sup> t rating for fusing ( 1ms< t < 10ms)	I <sup>2</sup> t	840	A <sup>2</sup> S
Type Forward Voltage at 30.0A	VF	0.93	V
Maximum Forward Voltage at 30.0 A			
Maximum DC Reverse Current @TA=25 °C at Rated DC Blocking Voltage @TA=125 °C	IR	10 500	μA
Typical Junction Capacitance Note1	Cj	50	pF
Operating and Storage Temperature Range	Tj, Tstg	-55 ~ +1	°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4 VDC.

2. Mounted on glass epoxy PC board with 4x1.5"x1.5" (3.81x3.81 cm) copper pad.

## RATINGS AND CHARACTERISTICS CURVES (TA = 25 °C unless otherwise noted)

Figure 1. Derating Curv

g

p



